

Document from the Organizing Committee of the Integrated Circuit EDA Elite Challenge

Announcement Regarding Invitations to the 2023 5th Integrated Circuit EDA Elite Challenge

To educational institutions of relevance:

We announce the 5th Integrated Circuit EDA Elite Challenge in 2023. It's in line with the national strategy for integrated circuit development, to serve China's IC industry ecosystem and foster deep integration between industry, academia, and research.

I. Name of the competition:

The event is titled: Integrated Circuit EDA Elite Challenge (referred to as "the Challenge" hereafter).

II. Purpose of the Challenge:

Aligning with the needs of EDA design industry evolution, the Challenge aims to amplify the innovative design and hands-on skills of college students in the IC EDA sector. By creating a platform for EDA algorithm and application exchanges, we aspire to enliven the campus environment of innovation and

entrepreneurship. This synergy between universities and IC-related businesses in nurturing talents is pivotal. It gears towards cultivating outstanding talents who possess innovative thinking, a spirit of teamwork, and the capability to tackle complex engineering problems. The ultimate goal? To fuel the robust and rapid growth of the EDA design industry.

III. Schedule of the Challenge

Milestone	Date
Announcement of the Challenge	August 18, 2023
Registration	August 19, 2023 - October 25, 2023
Topic Presentation and Guidance	August 2023 - November 2023
Submission of Project Reports	November 24, 2023
Final Project Submission	November 29, 2023
Semi-Final Technical Review	November 29, 2023 - December 7, 2023
Results Announcement	December 8, 2023 - December 10, 2023
Finals	Mid to late December 2023

IV. Competition Format

(1) Registration Method:

The competition is open to current master's, doctoral, and outstanding undergraduate students. All students are eligible to

participate. Contestants must provide photos of their ID cards and student IDs. (Newly admitted postgraduate students of the 2021 batch must also submit admission notices or relevant proofs.)

Each team consists of no more than three students and can have up to two supervising teachers. The team leader can register on the official competition website at <https://eda.icisc.cn/> and input team information (including team member details). This information should be updated before the registration deadline and cannot be changed afterward.

After the registration deadline, the organizing committee will review the eligibility of the teams based on their registration details. Only teams that pass this review will be granted participation rights.

(2) Competition Mode

The competition adopts specified topics and operates in an open format. The committee seeks topics from sponsoring companies related to the direction of the competition. These topics are then presented to the technical committee of the competition for review. Once approved, a competition topic guide is released to the public. During registration, each contestant can opt to team up for one "challenge topic" and one "regular topic". Alternatively,

they can choose just one of the two types. Teams must then submit their competition results in the manner and timeline specified in the notifications.

(3) Review Method:

1. Preliminary Review

The preliminary round is conducted online. Once teams have completed their respective topics, they should draft a technical report and any other materials required by the topic guide. Submissions are made via the official website before the competition's submission deadline. The technical committee of the competition reviews the entries online. Based on the committee's feedback, the organizing committee will finalize and announce the list of teams advancing to the finals on the competition website.

2. Final Review

Regular Topics: The final round adopts an on-site review approach. Teams that qualify for the finals will present on-site defenses during the time specified in the competition announcement. The overall score consists of both a technical score and the final defense score. Detailed proportions will be announced later.

Ranking Topics: Final rankings are based on technical scores. Award-winning students present their reports on-site, and absence without justification is considered a forfeiture of their position. The final rankings of the winning teams will be announced live, and teams are invited to present their reports on-site.

V. Selection Methods and Award Provisions

For regular topics, awards are categorized into first, second, and third tiers. Here, first-place awards will be capped at 15%, with second-place not exceeding 30%. Challenge topics, on the other hand, will award the top five participants based on technical scores.

On top of these accolades, from both regular and challenge topics, one exceptional team will be handpicked for an elite showcase vying for the prestigious "Kirin Cup" (one award) and the "Elite Cups" (two awards). These prizes are not cumulative, and the highest distinction will be given precedence in granting.

Furthermore, the Challenge will also honor outstanding mentors with the "Distinguished Guide Award" and recognize exemplary organization efforts with the "Exceptional Organization Award". Our valued sponsors have the liberty to bestow special corporate awards as they see fit.

Lastly, the technical committee of the Integrated Circuit EDA Elite Challenge will assess entries on their completeness, innovation, difficulty, quality of documentation, and defense presentations when deciding on the laurels.

VI. Additional Information

The Integrated Circuit EDA Elite Challenge doesn't charge any entry fee. However, travel expenses during the competition fall on the participating teams.

Both the organizing committee and the participating teams possess the rights to publish and use the design documents and related technical materials from the competition. Intellectual property rights for the entries remain with the participating teams.

Interpretation rights of this notice lie with the organizing committee of the Integrated Circuit EDA Elite Challenge.

Given the evolving COVID-19 situation, and in accordance with actual competition needs, changes may occur in competition dates, evaluation methods, etc. Always refer to the official announcements on our website <http://eda.icisc.cn/> and our WeChat platform. Stay updated!

For real-time student interactions, join our QQ groups:
792345737 / 569142323.

Contact: Mr./Miss Jiang, Secretariat of the Challenge

Tel: 13585200992

E-mail: jiangtian@icisc.cn

Integrated Circuit EDA Elite Challenge Organizing Committee

August 18, 2023.

